

PATENT
005702-20051

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Tomoharu TANAKA

Serial No: 09/456,340

Filed: December 8, 1999

For: **FLASH MEMORY CONTROL METHOD,
FLASH MEMORY SYSTEM USING THE
CONTROL METHOD AND FLASH
MEMORY DEVICE USING THE CONTROL
METHOD**



Art Unit: 2824

Examiner: Not yet assigned

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to:
Assistant Commissioner for Patents
Washington D.C. 20231, on

March 17, 2000

Date of Deposit

William H. Wright (RN 36,312)

Name

 3/17/00

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Date

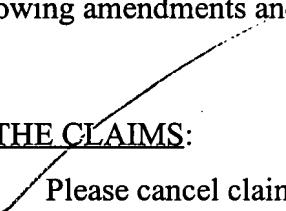
PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Prior to the first office Action in the present application, please enter and consider the following amendments and remarks:

IN THE CLAIMS:

 Please cancel claim 3 without prejudice.

REMARKS

Claim 3 is canceled without prejudice. Claims 1-2 and 4-22 are pending in the application.